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XA-9629

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Yutaka SHIMADA et al.

Appln. No.: 10/083,402

Group Art Unit: 1746

Filed: February 27, 2002

Examiner: Z. El Arini

For: MANUFACTURING METHOD OF SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE

\* \* \*

PETITION FOR EXTENSION OF TIME

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

It is respectfully requested that the time now set for response be extended, retroactively, by one month. If any further extension of time is required to avoid abandonment of this application, such extension is hereby requested. The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16 and 1.17 that may be required by this paper to Deposit Account No. 50-1165.

GWS:jab

Respectfully submitted,

Miles & Stockbridge P.C.  
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By: \_\_\_\_\_  
Mitchell W. Shapiro  
Reg. No. 31,568

By:   
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November 24, 2003